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Execution Date(s) 10/27/2008

- ☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ Joint Research Agreement  
☐ Government Interest Assignment  
☐ Executive Order 9424, Confirmatory License  
☐ Other \_\_\_\_\_

**2. Name and address of receiving party(ies)**

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City: Irvine

State: CA

Country: US Zip: 92617

Additional name(s) & address(es) attached? ☐ Yes ☒ No

**4. Application number(s) or patent number(s):**

A. Patent Application No.(s)  
See Attached Exhibit "A"

☐ This document is being filed together with a new application.

B. Patent No.(s)  
See Attached Exhibit "A"

Additional numbers attached? ☒ Yes ☐ No

**5. Name and address to whom correspondence concerning document should be mailed:**

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**6. Total number of applications and patents involved:** 117

**7. Total fee (37 CFR 1.21(h) & 3.41)** \$ 4680.00

- ☐ Authorized to be charged by credit card  
☒ Authorized to be charged to deposit account  
☐ Enclosed  
☐ None required (government interest not affecting title)

**8. Payment Information**

a. Credit Card Last 4 Numbers \_\_\_\_\_  
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Authorized User Name McAndrews, Held & Malloy

**9. Signature :** /Christopher C. Winslade/

December 17, 2008

Signature

Date

Christopher C. Winslade

Name of Person Signing

Total number of pages including cover  
sheet, attachments, and documents

11

**Exhibit A**

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>Remote-control device with directional audio system</b>	<b>11/839,798 08/16/07</b>		
<b>System and Method for Transferring a Multidimensional Array of Data to a Non-Contiguous Buffer</b>	<b>11/451,322 06/13/06</b>	<b>US 2008/0005499</b>	<b>01/03/08</b>
<b>A system and method of feature emphasis and de-emphasis in image processing</b>	<b>61/071,645</b>		
<b>Method and Apparatus for Managing Tasks in a Multiprocessor System</b>	<b>10/939,804 09/13/04</b>	<b>2006/0059484 A1</b>	<b>03/16/06</b>
<b>SIMD Processor with Register Addressing, Buffer Stall and Methods</b>	<b>10/929,992 08/30/04</b>	<b>2006/0047937 A1</b>	<b>03/02/06</b>
<b>SIMD Processor with Register Addressing, Buffer Stall and Methods</b>	<b>12/231,336 08/29/08</b>		
<b>Trellis Decoder for Decoding Data Stream including Symbols Coded with Multiple Convolutional Codes</b>	<b>10/973,486 10/26/04</b>	<b>2006/0088119 A1</b>	<b>04/27/06</b>
<b>SIMD Processor having Enhanced Operand Storage Interconnects</b>	<b>10/940,150 09/13/04</b>	<b>2006/0095712 A1</b>	<b>05/04/06</b>
<b>SIMD Processor Executing Min/Max Instructions</b>	<b>10/940,123 09/13/04</b>	<b>2006/0095739</b>	<b>05/04/06</b>
<b>SIMD Processor Executing Min/Max Instructions</b>	<b>12/231,891 09/05/08</b>		
<b>Digital Receiver Having Adaptive Carrier Recovery Circuit</b>	<b>10/757,536 01/15/04</b>	<b>7,342,981</b>	<b>03/11/08</b>

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>Audio/Video Separator</b>	10/812,279 03/29/04	2005/0212977 A1	09/29/05
<b>Worldwide Analog Television Signal Receiver</b>	10/940,153 09/13/04	7,394,500	07/01/08
<b>Film-Mode (3:2/2:2 Pulldown) Detector, Method and Video Device</b>	10/837,835 05/03/04	2005/0243215	11/03/05
<b>Low Latency Digital Filter and Method</b>	11/090,478 03/25/05	2006/0215746	09/28/06
<b>Method and Apparatus for Using the Host-Pod Interface of a Digital Television or Other Digital Audio/Video Receiver for New Services</b>	11/306,317 12/22/05	2007/0146542 A1	06/28/07
<b>Methods and Apparatus for Processing Variable Length Coded Data</b>	11/046,048 01/28/05	7,132,963	11/07/06
<b>Methods and Apparatus for Processing Variable Length Coded Data</b>	11/538,336 10/03/06	7,385,534	06/10/08
<b>Methods and Apparatus for Processing Variable Length Coded Data</b>	12/133,489 06/05/2008		
<b>Video Receiver Providing Video Attributes with Video Data</b>	11/402,902 04/13/06	2007/0262979 A1	11/15/07
<b>Image Analyser and Adaptive Image Scaling Circuit and Methods</b>	11/619,358 01/03/07		
<b>Enhanced Video Processing Using Motion Vector Data</b>	12/032,796 02/28/08		
<b>Low Latency Cadence Detection for Frame Rate Conversion</b>	11/616,192 12/26/06	2008/0151109 A1	06/26/08

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>Video Processor Architecture and Method for Frame Rate Conversion</b>	<b>11/616,188 12/26/06</b>	<b>2008/0151108 A1</b>	<b>06/26/08</b>
<b>Harmonic Edge Synthesizer, Enhancer and Methods</b>	<b>11/619,366 01/03/07</b>	<b>2007/0152991 A1</b>	<b>07/05/07</b>
<b>Device and Methods for Reducing Visual Artifacts in Color Images</b>	<b>11/848,366 08/31/07</b>		
<b>Adaption of Frame Rate Converter Output Target Display Times Based on Video Content Detection</b>	<b>61/071,470 04/30/08</b>		
<b>Apparatus and Method for Synchronizing an SCDMA Upstream or any other...</b>	<b>09/074,036 05/06/98</b>	<b>6,243,369</b>	<b>06/05/01</b>
<b>Apparatus and Method for Synchronizing an SCDMA Upstream...</b>	<b>09/798,845 03/01/01</b>	<b>6,353,604 B2</b>	<b>03/05/02</b>
<b>Two Dimensional Interleave Process for CDMA...</b>	<b>09/152,643 09/14/98</b>	<b>6,466,564</b>	<b>10/15/02</b>
<b>Mixed DOCSIS 1.0 TDMA Burst with SCDMA...</b>	<b>09/337,167 06/21/99</b>	<b>6,459,703</b>	<b>10/01/02</b>
<b>Method and Apparatus for Faster Block Size...</b>	<b>09/648,278 08/23/00</b>	<b>6,848,036</b>	<b>01/25/05</b>
<b>Method and Apparatus to Improve SCDMA...</b>	<b>09/974,202 10/09/01</b>	<b>7,002,899 B2</b>	<b>02/21/06</b>

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>Method and Apparatus to Improve SCDMA...</b>	<b>11/326,225 01/04/06</b>		
<b>Down-Stream Transmitter and Cable...</b>	<b>10/944,962 09/18/04</b>	<b>20060085727</b>	<b>04/20/06</b>
<b>Downstream transmitter and cable modem receiver for 1024 QAM</b>	<b>12/286,337 09/29/08</b>		
<b>Blind Cost Criterion Timing Recovery</b>	<b>09/761,303 01/17/01</b>	<b>6,985,549</b>	<b>01/10/06</b>
<b>Carrier Phase Estimation Based On Single-Axis Constant Modulus Cost Criterion And Bussgang Criteria</b>	<b>09/922,006 08/03/01</b>	<b>7,031,405</b>	<b>04/18/06</b>
<b>Linear Prediction Based Initialization of A Single-Axis Blind Equalizer for VSB Modulations</b>	<b>10/016,665 12/11/01</b>	<b>7,027,500</b>	<b>04/11/06</b>
<b>DC Offset Correction For Constant Modulus Equalization</b>	<b>10/213,854 08/07/02</b>	<b>7,349,469</b>	<b>03/25/08</b>
<b>Rate Control for Digital Video Compression Processing</b>	<b>11/049,103 02/02/05</b>	<b>US-2006-0171457- A1</b>	<b>08/03/06</b>
<b>Picture adjustment methods and apparatus for image display device</b>	<b>11/504,496 08/15/06</b>	<b>20080043031A1</b>	<b>2/21/08</b>
<b>Method of estimating trellis encoded symbols utilizing simplified trellis decoding</b>	<b>09/099,730 06/19/98</b>	<b>6,178,209</b>	<b>01/23/01</b>
<b>Reduced complexity equalizer for multi mode signaling</b>	<b>09/100,705 06/19/98</b>	<b>6,426,972</b>	<b>07/30/02</b>
<b>Data slicer for combined trellis decoding and equalization</b>	<b>09/716,651 11/20/00</b>	<b>6,850,563</b>	<b>02/01/05</b>

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>Dual loop automatic gain control</b>	10/253,245 09/24/02	7,116,955	10/3/06
<b>Vertex data processing with multiple threads of execution</b>	09/632,759 8/4/00	6,963,347	11/8/05
<b>Vertex data processing with multiple threads of execution</b>	11/250,754 10/14/05	US 2006-0033757 A1	02/16/06
<b>Memory request interlock</b>	09/638,550 8/14/00	6,810,470	10/26/04
<b>Method and apparatus for detecting and correcting modulated signal impairments (phase noise)</b>	11/947,090 11/29/07	US-2008-0123788- A1	05/29/08
<b>Method and apparatus for detecting and correcting modulated signal impairments (burst noise)</b>	11/947,124 11/29/07	US-2008-0123787- A1	05/29/08
<b>Method and apparatus for detecting and correcting modulated signal impairments (gain noise)</b>	11/947,105 11/29/07	US-2008-0123786- A1	05/29/08
<b>Method to detect and mitigate impulse noise on receivers</b>	11/775,836 7/10/07		
<b>Framebuffer sharing for video processing</b>	11/847,802 08/30/07	US-2008-0165287- A1	07/10/08
<b>High performance embedded memory and I/O bridge design in SOC</b>	11/871,120 10/11/07		
<b>Method and system for motion compensated noise reduction</b>	11/847,820 08/30/07	US-2008-0055477- A1	03/06/08
<b>Unified memory architecture for T-CON end back-end video processor ICS (Integrated Circuits)</b>	11/874,659 10/18/07	US-2008-0198937- A1	08/21/08

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>Halo artifact removal method</b>	11/960,991 12/20/07		
<b>Method and apparatus for impulse noise detection and suppression for DVB-T</b>	11/964,373 12/26/07		
<b>Method and apparatus for reduced complexity video processing via special chroma handling</b>	12/126,321 05/23/08		
<b>H.264 data processing</b>	11/897,714 08/30/07	US-2008-0165860- A1	07/10/08
<b>Video stream timing detection</b>	11/839,361 08/15/07	US-2008-0143876- A1	06/19/08
<b>Video decoder block</b>	11/847,879 08/30/07	US-2008-0056357- A1	03/6/08
<b>Neighboring context management</b>	11/842,901 08/21/07	US-2008-0056377- A1	3/6/2008
<b>Method and system for providing error resiliency</b>	11/735,085 4/13/2007		
<b>A system and method of motion vector estimation using content associativity</b>	11/966,095 12/28/07		
<b>Hybrid memory compression scheme for decoder bandwidth reduction</b>	11/971,045 01/08/08		
<b>Method and System for Receiving and Framing Packetized Data</b>	09/490,350 01/24/00	6,763,390	07/13/04
<b>Method and System for Receiving and Framing Packetized Data</b>	10/818,812 04/6/04	7,376,692	05/20/08
<b>Method and System for Improved Display Filtering</b>	09/790,955 02/22/01	6,710,819	03/23/04

Title	Application	Pub. No./	Pub. Date/
	Number/ Filing Date	Patent No.	Issue Date
Method and Apparatus for Processing Video and Graphics Data	09/923,516 080/7/01	6,828,987	12/07/04
Tiled Memory Configuration for Mapping Video Data and Method Thereof	09/923,520 08/07/01	7,016,418	02/21/06
System For Testing Multiple Devices on a Single System and Method Thereof	09/923,524 08/07/01	7,253,818	080/7/07
Memory Controller for Handling Multiple Clients and Method Thereof	11/627,585 01/26/07	2007/0124793 A1	05/31/07
Method and System for Accessing Packetized Elementary Stream Data	09/489,682 01/24/00	6,778,533	08/17/04
Method and System for Retrieving Adaptation Field Data Associated with a Transport Packet	09/490,207 01/24/00	6,785,336	08/31/04
Method and Apparatus for Handling Private Data from Transport Stream Packets	09/491,122 01/24/00	6,804,266	10/12/04
Method for Displaying Data	09/489,681 01/24/00	6,999,424	2/14/06
Method and System for Generating Transport Stream Packets	11/333,811 01/17/06	2006/0209709 A1	09/21/06
A Method and System for Handling Errors and a System for Receiving Packet Stream Data	09/489,669 1/24/2000	6,988,238	01/17/06
Method and System for Handling Errors	10/919,943 08/17/04	7,366,961	04/29/08
Method and System for Handling Data Packets	09/791,045 2/22/2001	6,807,585	10/19/04
Method and System for Handling Errors	12/031,584 02/14/08	N/A	N/A
System for Controlling a Clock Signal for Synchronizing a Counter to a Received Value and Method Thereof	09/563,209 5/2/2000	6,674,805	01/06/04
System for Handling compressed Video Data and Method Thereof	09/563,211 5/2/2000	7,113,546	09/26/06
System for Bit-Rate Controlled Digital Stream Playback and Method Thereof	09/800,226 3/6/2001	6,907,481	06/14/05
System for Digitized Audio Stream Synchronization and Method Thereof	09/800,224 3/6/2001	7,030,930	04/18/06



<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
<b>System for Digital Stream Transmission and Method Thereof</b>	<b>09/800,223 03/06/01</b>	<b>7,047,330</b>	<b>05/16/06</b>
<b>System for Frame Based Audio Synchronization and Method Thereof</b>	<b>09/833,493 04/11/01</b>	<b>7,130,316</b>	<b>10/31/06</b>
<b>Method for synchronizing to a data stream</b>	<b>09/491,119 01/24/00</b>	<b>6,885,680</b>	<b>04/26/05</b>
<b>Systems, methods, and apparatus for synchronization of audio and video signals</b>	<b>11/754,096 05/25/2007</b>	<b>20070276670A1</b>	<b>11/29/07</b>
<b>Method and apparatus for deinterlacing video</b>	<b>09/539,572 03/31/2000</b>	<b>6,680,752</b>	<b>01/20/04</b>
<b>Method for deinterlacing interlaced video by a graphics processor</b>	<b>09/553,143 04/20/2000</b>	<b>6,970,206</b>	<b>11/29/05</b>
<b>Diversity receiver with joint signal processing</b>	<b>09/363,813 07/30/1999</b>	<b>6,560,299</b>	<b>05/06/03</b>
<b>Diversity receiver with joint signal processing</b>	<b>10/384,048 03/07/03</b>	<b>6,970,523</b>	<b>11/29/05</b>
<b>Diversity receiver with joint signal processing</b>	<b>10/409,371 04/09/03</b>	<b>6,870,892</b>	<b>03/22/05</b>
<b>Diversity receiver with joint signal processing</b>	<b>10/408,881 04/09/03</b>	<b>6,937,677</b>	<b>08/30/05</b>
<b>Method and apparatus for generating a video overlay</b>	<b>09/678,362 10/03/00</b>	<b>6,788,309</b>	<b>09/07/04</b>
<b>Method and apparatus for reducing instruction dependencies in extended SSA form instructions</b>	<b>10/954,586 09/30/04</b>	<b>2006-0070050</b>	<b>03/30/06</b>
<b>Automatic audio and video synchronization</b>	<b>10/907,073 03/18/05</b>	<b>2006-0209210</b>	<b>09/21/2006</b>

<b>Title</b>	<b>Application Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
Receiver for robust data extension for 8VSB signaling	10/251,650 09/20/02	7,194,047	03/20/07
Receiver for robust data extension for 8VSB signaling	11/725,571 03/19/07	2007-237263	10/11/07
Hybrid Soft and Hard Decision Feedback Equalizer	09/549,368 04/14/00	7,006,565	02/28/06
Adaptive Equalizer with Enhanced Error Quantization	09/231,888 01/14/99	6,418,164	07/09/02
Digital Pixel Clock Generation Circuit and Method Employing Independent Clock	10/908,429 05/11/05	2006-0256102	11/16/06
Equalizer method and apparatus using constant modulus algorithm blind equalization and partial decoding	09/457,496 12/09/99	6,668,014	12/23/03
Adaptive equalizer with decision directed constant modulus algorithm	09/261,843 03/03/99	6,337,878	01/08/02
Method and apparatus for encoding video color information	08/761,642 12/06/96	6,124,899	09/26/00
Intra motion prediction for response time compensation	11/864,362 09/28/07		
Response time compression using a complexity value of image information	11/864,391 09/28/07		
Compression Method and Apparatus for Response Time Compensation	11/864,412 09/28/07		
Spread spectrum fifo and method for storing data for multiple display types	09/484,367 01/14/00	6,580,432	06/17/03
Method and apparatus for processing image data	12/144,018		

<b>Title</b>	<b>Application</b>		
	<b>Number/ Filing Date</b>	<b>Pub. No./ Patent No.</b>	<b>Pub. Date/ Issue Date</b>
	06/23/08		
<b>Image processing method and system with repetitive pattern detection</b>	61/129,916 07/29/08		
<b>Adaptation of frame rate converter output target display times based on video content detection</b>	61/071,470 04/30/08		
<b>Method and apparatus for transmitting and using picture descriptive information in a frame rate conversion processor</b>	12/174,738		
<b>Null detection and erasure decoding for frequency selective channel in a broadcasting system</b>	12/143,434		